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100

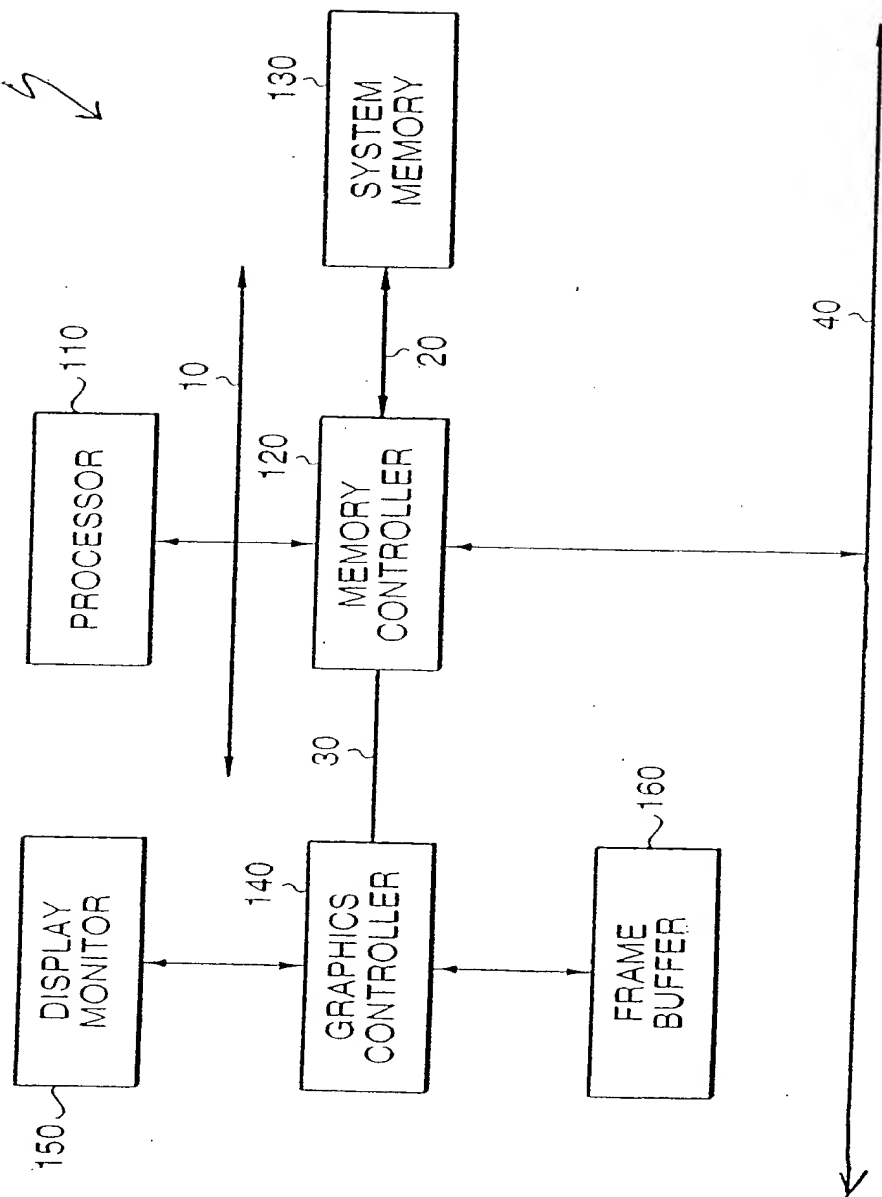


FIG. 1

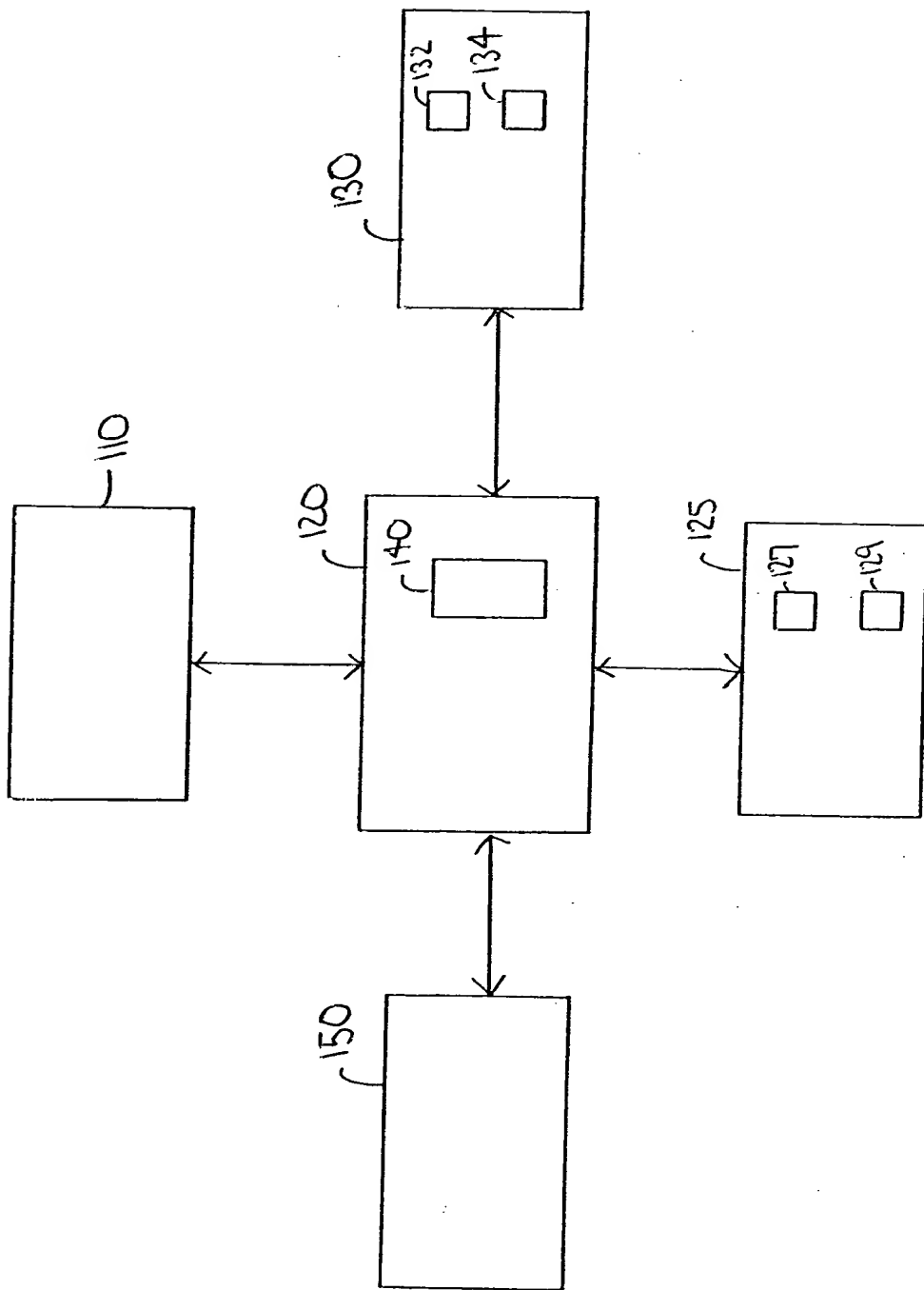


FIG. 2

FIG. 3

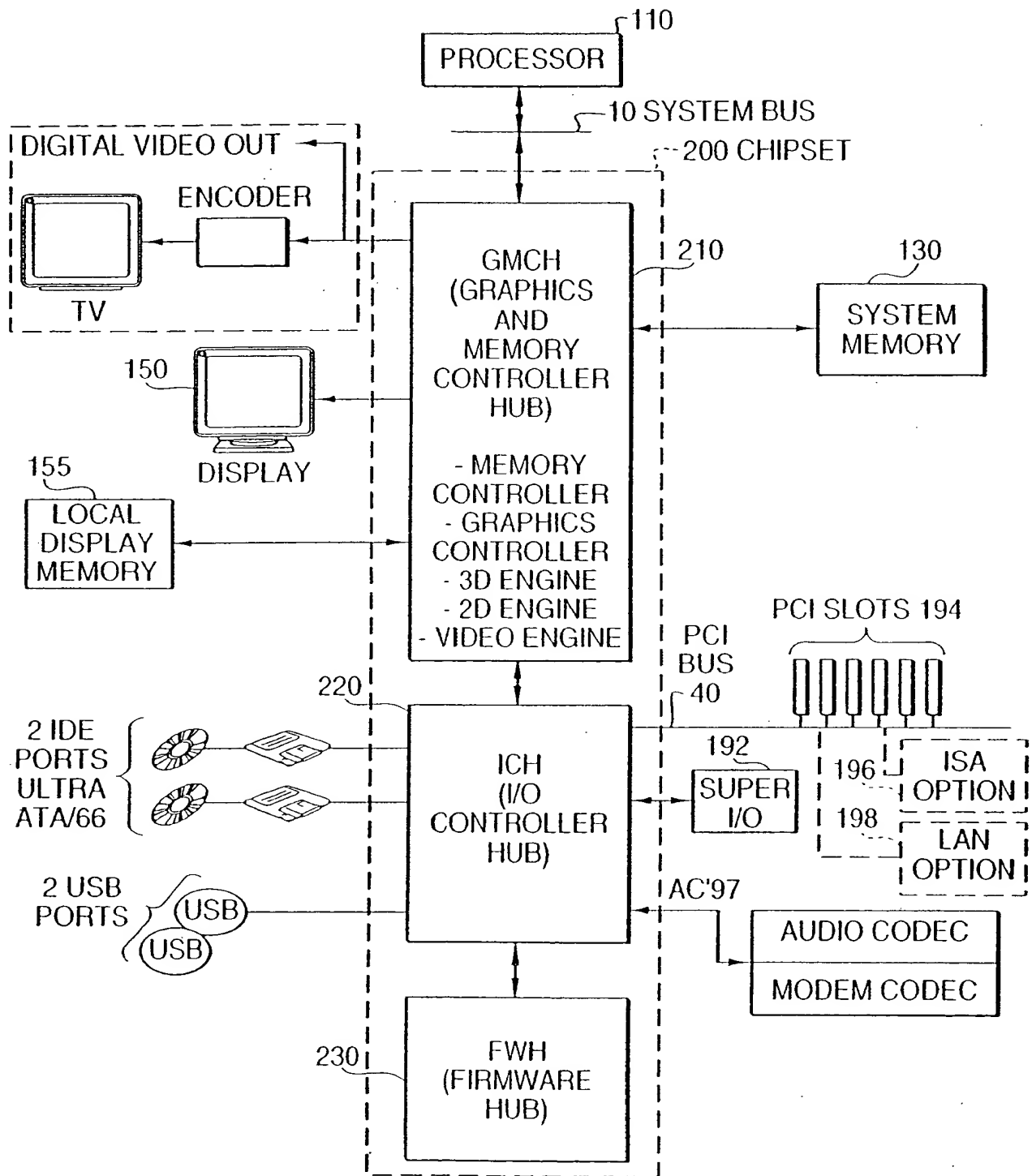
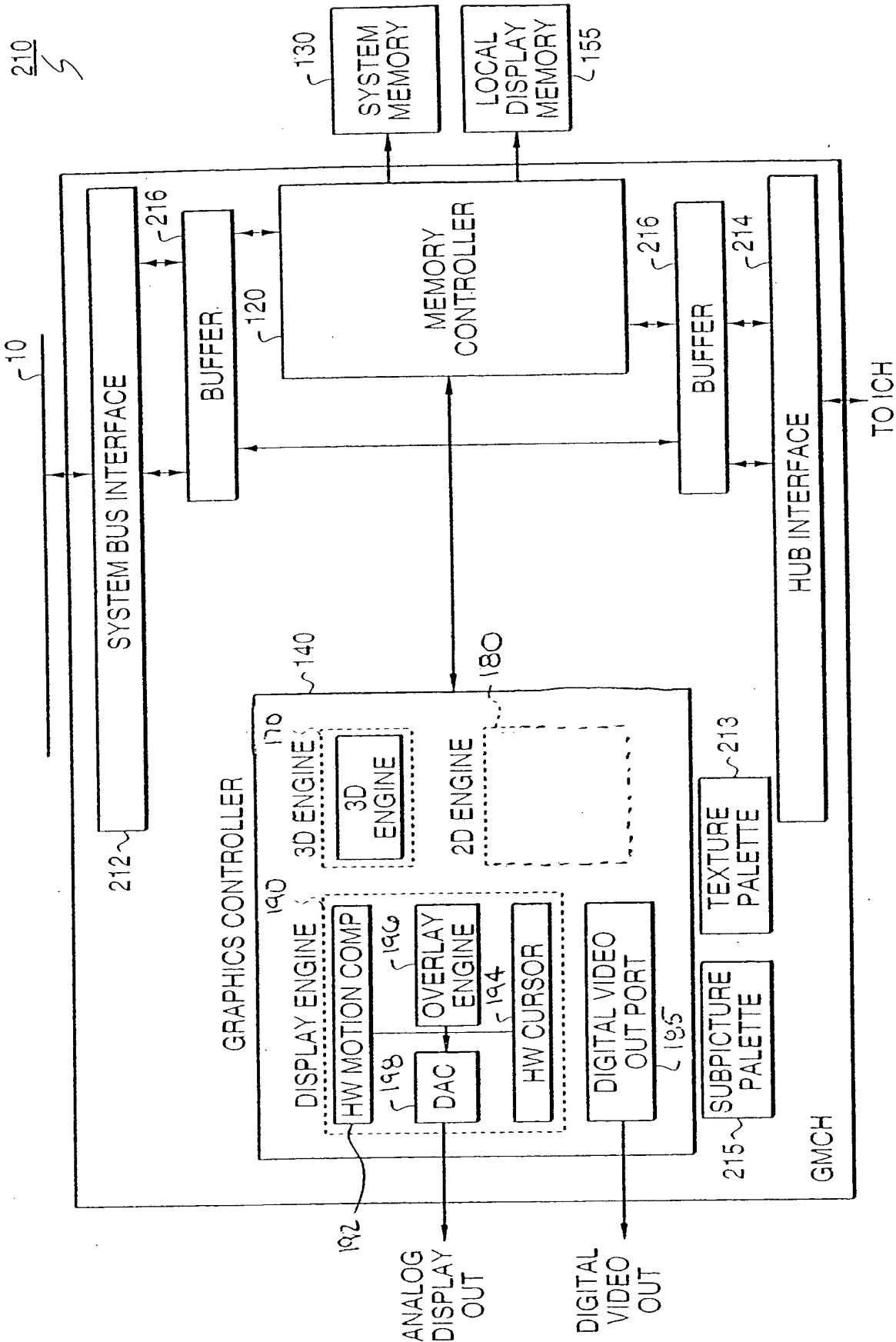


FIG. 3

FIG. 4



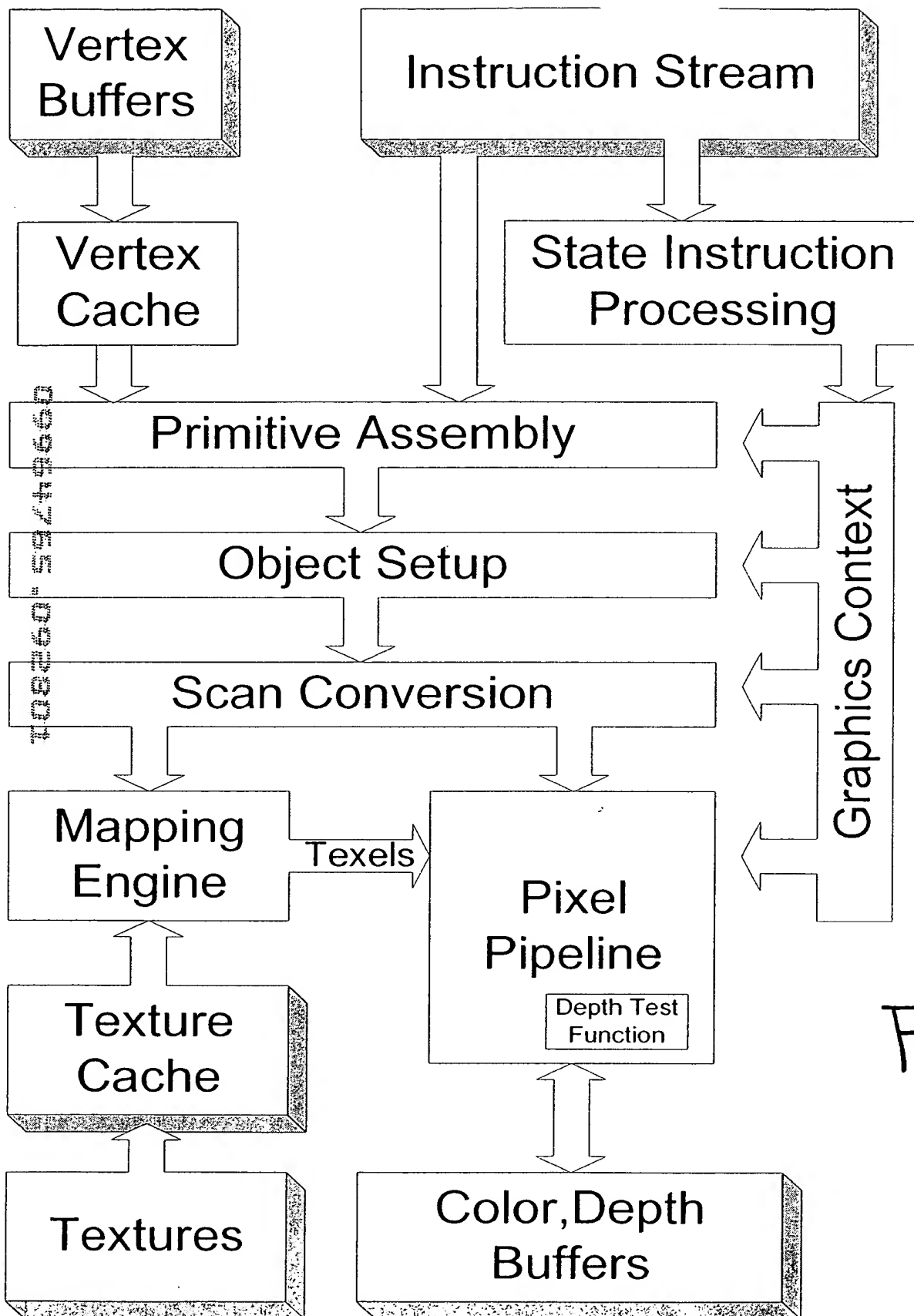


FIG. 5

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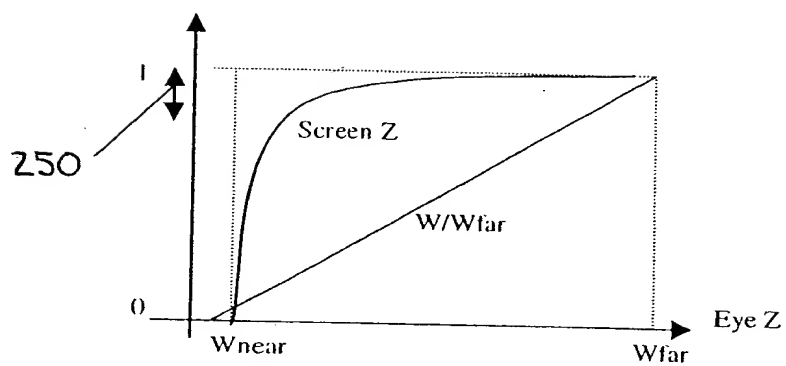


FIG. 6

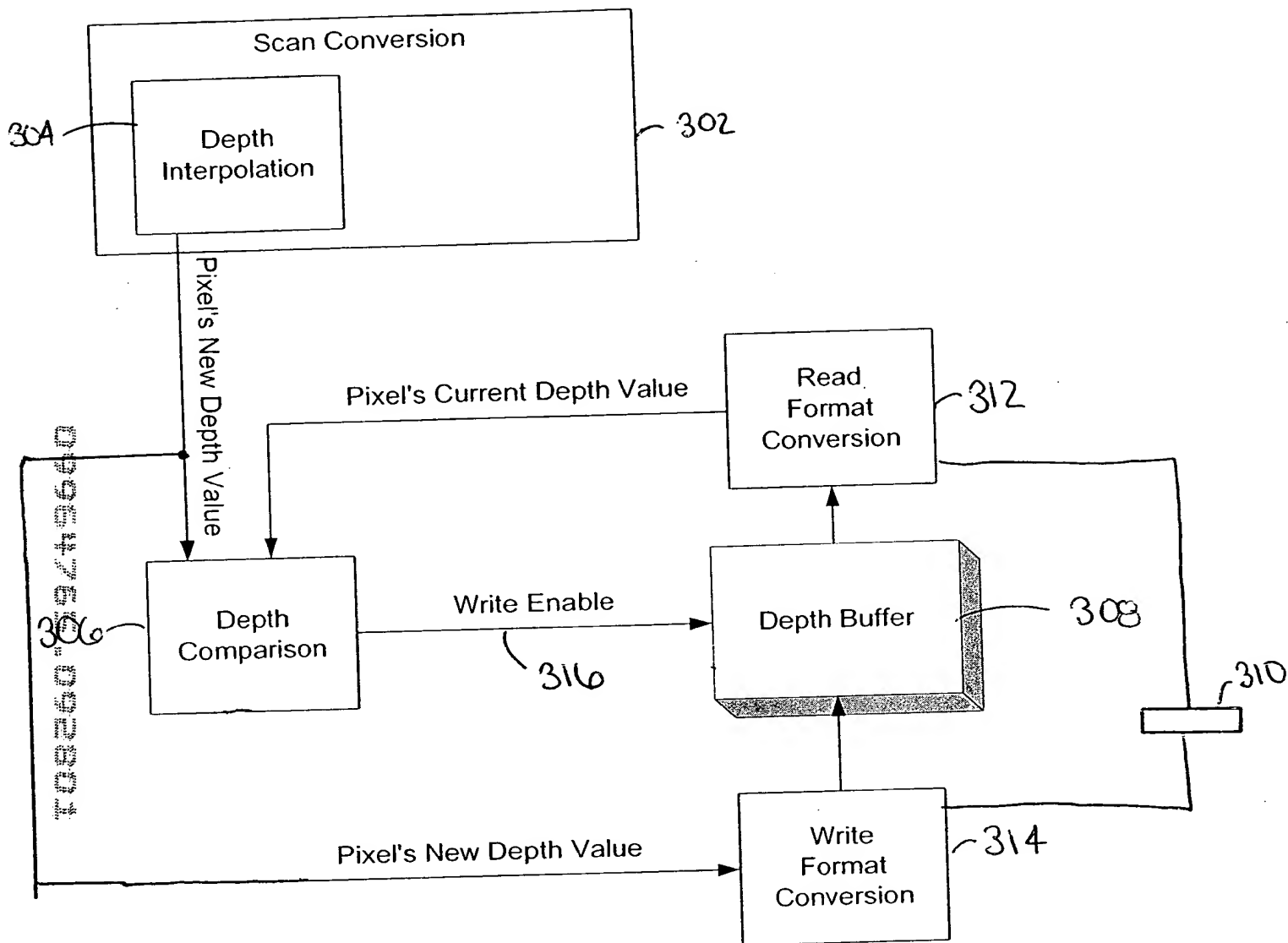
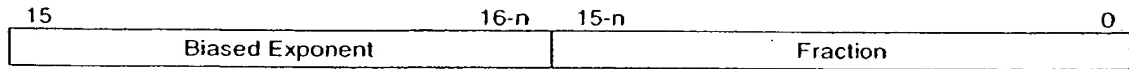


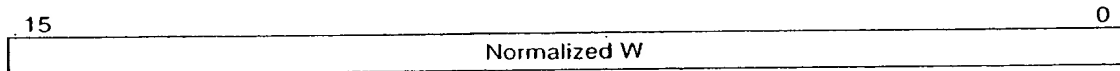
FIG. 7

FIG. 8A



Bit	Description
15:16-n	Biased Exponent: Format: n-bit unsigned biased exponent, where $n = WExponentSelect$. The exponent is biased by 2^n .
15-n:0	Fraction: Format: (16-n)-bit fractional portion of the floating point significand.

FIG. 8B

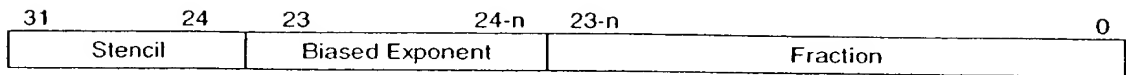


Bit	Description
15:0	Normalized W (W/Wfar): Format: U0.16 Range = [0,1)

FIG. 8C

Biased Exponent (n bits)	Significand		Represented Value (W/WFar)
	Integer	Fraction	
$exp = 0..2^n - 1$	1	frac	$1.frac * 2^{(exp-2^n)}$

FIG. 9A



Bit	Description
31:24	Stencil: Format: U8 Range = [0,255]
23:24-n	Biased Exponent: Format: n-bit unsigned biased exponent, where $n = WExponentSelect$. The exponent is biased by 2^n .
23-n:0	Fraction: Format: (16-n)-bit fractional portion of the floating point significand.

FIG. 9B



Bit	Description
31:24	Stencil: Format: U8 Range = [0,255]
23:0	Normalized W (W/Wfar): Format: U0.24 Range = [0,1]

FIG. 9A-59249652